

## Newsletter 24-25

### Department of Electronics and Telecommunication Engineering Gharda Institute of Technology Lavel, Ratnagiri.



- **INTRODUCTION:**

Department of Electronics and Telecommunication Engineering has been established in year 2009. The Department offers a Four Year Bachelor of Engineering Degree Program in Electronics and Telecommunication Engineering. It has well experienced and qualified faculty members as well as technical assistants. Total student strength of the department is 71. Department has well equipped classrooms with smart boards, audio/Video Facility, LCD Projector, OHP's, Multimedia PC, Internet facility. There are 8 well-equipped laboratories in the department. The department also tops in organizing employability development workshops/seminars for students and staff which are really fruitful and outcome based.

- **PULSE ACTIVITIES:**

The Department Of EXTC is having student's association 'PULSE' stands for 'Pioneer Union for lighting the science of Engineering'. Prof. S. N. Pandharkame is working as

PULSE coordinator for the academic year 2024-25. All the activities regarding workshops, fresher's party, Farewell, seminars are taken by PULSE committee. For the successful conduction of all the following events, PULSE team worked very hard under the guidance of PULSE coordinator Prof. S. N. Pandharkame and HOD EXTC Prof. Dr. S. R. Khedekar.

Sr. No	Post	Name of Student
1.	President	Miss. Nagarkar Saloni
2.	Voice President	Miss. Bolade Sanika
3.	Secretary	Miss. Chalke Seva
4.	Co- Secretary	Mr. Deshmukh Omkar
5.	Treasurer	Mr. Taldevkar Ninad
6.	Co-Treasurer	Mr. Shinde Manish
7.	Decoration Head	Miss. Sanika Nayakal & Mr. Kunal Patil
8.	Technical Head	Mr. Wani Prathmesh
9.	Event Organizer	Miss. Kadam Anushka & Miss. Dhadve Amita
10.	Sports Head	Mr. Ghag yash
11.	Document Keeper	Miss. Gorivale Vaibhavi & Miss. Shinde Ayushi
12.	Media	Miss. Mohite Sayali & Miss. Udeg Riya
13.	Anchoring	Mr. Desai Omkar
14.	Member	Miss. Mahadik Triveni & Mr. Shirke Vrushabh

#### **PULSE ACTIVITIES IN 2024-25**

Sr. no	Name of the activity	Date	No. of students
1	NPTEL Awareness Workshop	19.07.2024	40
2.	Seminar on 'Awareness about Idea, Innovation, Incubation and Entrepreneurship Cell'	24.07.2024	18
3	Webinar on 'VLSI/semiconductor industry'	05.08.2024	56
4	Seminar on 'How to write research / technical paper'	07.08.2024	45

5	Webinar on ‘Advances in RedHat Linux and DevOps’	09.08.2024	30
6	Seminar on ‘NOC, SOC opportunities and various Certification paths	16.08.2024	19
7	Traffic Awareness Program	23.01.2025	30
8	Seminar On Mastering Manners And Etiquette About Women	05.02.2025	27
9	Gender Sensitization	05.02.2025	20
10	Webinar On Data Science & Analytics	12.02.2025	45
11	Industrial Visit At Seiler Garepa India Pvt. Ltd.	14.02.2025	30

## **A Report on “NPTEL Awareness Workshop”**

### **Event Overview:**

National Programme on Technology Enhanced Learning ( initiated by seven Indian Institutes of Technology ( Guwahati and Roorkee) along with the Indian Institute of Science, Bangalore in 2003, to provide quality education to anyone interested in learning from the IITs. The main goal was to create web and video courses in all major branches of engineering and physical sciences at the undergraduate and postgraduate levels and management courses at the postgraduate level. “NPTEL awareness Seminar” was organized by IIT Madras in association with Gharda Institute of Technology, Lavel and Aditya College of Engineering Andhra Pradesh on 19.07.24 at 10.00 to 12.00 noon in online mode.



LIVE SESSION

# NPTEL AWARENESS E-WORKSHOP

In Association with

**ADITYA COLLEGE OF ENGINEERING**

Chittoor, Andhra Pradesh

**GHARDA INSTITUTE OF  
TECHNOLOGY**

Ratnagiri, Maharashtra

Date & Time: July 19, 2024, 10:00 AM



Link: <https://youtube.com/live/Muy8tglOPTI?feature=share>

## Objective:

The primary objectives of the workshop were:

1. **Raising Awareness:** Educating attendees about the NPTEL platform, its purpose, and the wide range of courses it offers across various disciplines including engineering, science, humanities, and management.
2. **Demonstrating Usability:** Providing a hands-on demonstration of how to register for courses, access course materials, and apply for certifications. This also included a walkthrough of the platform's user interface.
3. **Encouraging Participation:** Motivating students and professionals to take advantage of the free and accessible resources offered by NPTEL to enhance their knowledge and skills.
4. **Showcasing Success Stories:** Sharing success stories of individuals who have leveraged NPTEL certifications to advance their academic and professional careers.

## Workshop Agenda:

The workshop was structured into several key sessions:

1. **Introduction to NPTEL:** An overview of the NPTEL initiative, its history, and its objectives.
2. **Course Enrollment Process:** A step-by-step guide on how to navigate the NPTEL website, search for courses, enroll, and access course content.
3. **Certification Process:** Detailed information on how to register for exams, the importance of certification, and its recognition by industries and academic institutions.
4. **Interactive Q&A Session:** Participants were encouraged to ask questions and clarify any doubts they had regarding the platform or the courses available.
5. **Testimonials:** Alumni who have successfully completed NPTEL courses shared their experiences and the impact it had on their careers.

### **Key Outcomes:**

The workshop was successful in achieving its objectives. Key outcomes include:

1. **Increased Awareness:** Participants left with a clear understanding of the benefits of NPTEL and how it can be a valuable resource for both students and professionals.
2. **Higher Engagement:** Many attendees expressed their intention to enroll in NPTEL courses, particularly those aligned with their academic or professional goals.
3. **Networking Opportunities:** The event facilitated networking among participants, fostering a community of learners who can support each other in their educational journeys.
4. **Feedback and Suggestions:** The feedback gathered from participants highlighted the need for more such workshops, particularly in rural and remote areas, to broaden the reach of NPTEL.

### **Conclusion:**

The NPTEL Awareness Workshop was a resounding success, effectively increasing knowledge and engagement with the NPTEL platform. By providing detailed guidance on how to utilize this valuable resource, the workshop has empowered participants to take control of their learning and professional development. Future workshops should continue to focus on expanding reach and ensuring that more students and professionals can benefit from NPTEL's offerings.

## Photographs:



## Report of seminar on 'Awareness about Idea, Innovation, Incubation and Entrepreneurship Cell'

The department of E&TC has successfully conducted a seminar on Awareness about Idea, Innovation, Incubation and Entrepreneurship Cell. The following are the details of the same.

Date: 24/07/2024

Time: 04.00 Pm to 05.00 Pm

Venue: BE Classroom

Total Number of students attended the seminar: 18

Name of the resource person: Dr. Samir Gajmal

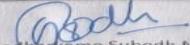
The awareness program aims to introduce students to the functions and opportunities provided by the Idea, Innovation, Incubation, and Entrepreneurship Cell (I3EC) of our college. It will cover topics such as: Introduction to I3EC and its role in promoting innovation and entrepreneurship among students, Opportunities and support available for student startups and innovative projects, Success stories of alumni and current students who have benefited from the I3EC initiatives, How to get involved and benefit from the resources offered by the I3EC.

Some snaps of the same are shown below.



Attendance of the seminar is shown below.

Attendance sheet Awareness program " Idea, innovation Incubation & Entrepreneurship			
DATE: 24/07/2024			
SR.NO.	NAME OF STUDENTS	CLASS SE/TE/BE	Sign
1	Amey Dattatray shembekar	EXTC (SE)	Amey
2	Sudarshan Shankar Shinde	EXTC (SE)	Sudarshan
3	Anvesh Sanjay Daiv	EXTC (SE)	Anvesh
4	Amit Bhalechandra Kokade	EXTC (SE)	Amit
5	Dhananjay Chandrakant Gatal	EXTC (SE)	Dhananjay
6	Ars h Aslam Chiplunkar	EXTC (SE)	Aslam
7	Srothak Sanjay Sawant	EXTC (SE)	Srothak
8	Bhavik Manoj Pawasikar	EXTC (SE)	Bhavik
9	Kunal Suresh Patil	EXTC (SE)	Kunal
10	Ayush Arvind Khambe	EXTC (SE)	Ayush
11	Nihal Iqbal Bora	EXTC (SE)	Nihal
12	Madhura Diyambar Hattikar	EXTC (SE)	Madhura
13	Bhagyashri Santosh Kherade	EXTC (SE)	Bhagyashri
14	Apurva shrikrishna Kadam	EXTC (SE)	Apurva
15	Anushka chandramani Mahadik	EXTC (SE)	Anushka
16	Amruta Manohar Dhadve	EXTC (SE)	Amruta
17	Anushka Rajesh Kadam	EXTC (SE)	Anushka
18	Prathmesh Jandip Wani	EXTC (SE)	Prathmesh
19			
20			
21			
22			
23			
24			
25			
26			
27			
28			
29			
30			



Prof. Pandharkarne Subodh N  
Pulse Co-Ordinator



Dr. Khedekar Sachin R  
HOD/EXTC

This Seminar was covered functions and opportunities provided by the Idea, Innovation, Incubation, and Entrepreneurship Cell (I3EC) of our college. It covered topics such as: Introduction to I3EC and its role in promoting innovation and entrepreneurship among students, Opportunities and support available for student startups and innovative projects, Success stories of alumni and current students who have benefited from the I3EC initiatives, How to get involved and benefit from the resources offered by the I3EC available placement opportunities, The seminar was very knowledgeable, interactive and informative. Total 18 Students attended the Seminar.

### Report of Webinar on 'VLSI/semiconductor industry'

The department of E&TC has successfully conducted a online webinar on VLSI/semiconductor industry. The following are the details of the same.

Date: 05/08/2024

Time: 10.00 Am to 12.00 Pm

Venue: TE Classroom

Total Number of students attended the seminar: 56

Name of the staff Co-ordinators: Mr. S. N. Pandharkame

Name of the resource person: Miss. Hena Takshila

VLSI, or Very-Large-Scale Integration, is a process in semiconductor manufacturing that involves creating integrated circuits (ICs) by combining thousands to millions of transistors on a single chip. VLSI technology is fundamental to the development of microprocessors, memory chips, and other complex electronic circuits, which are integral to modern electronic devices. VLSI technology has enabled the rapid advancement of electronics, leading to smaller, faster, and more efficient devices. It plays a crucial role in the development of modern computing and communication systems.

Some snaps of the same are shown below.





Attendance of the seminar is shown below.

Attendance sheet <i>Webinar on VLSI/Semiconductor Industry</i>			
		DATE: 05/08/2024	
SR.NO.	NAME OF STUDENTS	CLASS SE/TE/BE	Sign
1	Kirti B. Naik	TE	<i>Naik</i>
2	Sudanshan Shankar Shinde	SE	<i>Shinde</i>
3	Harshad. V. chogale	SE	<i>Chogale</i>
4	Triveni J. Mahadik	TE	<i>Triveni</i>
5	Sanika A. Ambre	BE	<i>Ambre</i>
6	Musken M. Bandarkar	BE	<i>Musken</i>
7	Sanika Dhananjay Nayakal	TE	<i>Nayakal</i>
8	Madhura Digambar Hattal	SE	<i>Hattal</i>
9	Madhura Mayur Jathar	TE	<i>Jathar</i>
10	Ghag Yash Santosh	BE	<i>Ghag</i>
11	Ghantekar Swapnil Amant	BE	<i>Ghantekar</i>
12	Ayfan Ismail Mahadik	BE	<i>Ismail</i>
13	Zaid Nareem Ansari	BE	<i>Ansari</i>
14	Nihal Iqbal Boat	SE	<i>Boat</i>
15	Prunav Vijay Waje	SE	<i>Waje</i>
16	Omkar Desai	TE	<i>Desai</i>
17	Yasir Bagdade	BE	<i>Bagdade</i>
18	Chavale Dilipkumar kadam	BE	<i>Chavale</i>
19	Piyush Pravin Phalke	SE	<i>Phalke</i>
20	Yash Anilbhai Gajre	TE	<i>Gajre</i>
21	Omkar Chandeobhai Deshmukh	TE	<i>Deshmukh</i>
22	Ayan Gulyzar Dhakle	BE	<i>Dhakle</i>
23	Fidyan Urman Jambhariboy	TE	<i>Jambhariboy</i>
24	Amrit Manohar Dhadwe	TE	<i>Dhadwe</i>
25	Anushka Chandramani Mahadik	SE	<i>Mahadik</i>
26	Purna Sudas Rasal	TE	<i>Rasal</i>
27	Saloni Pradip Nagnarkar	TE	<i>Nagnarkar</i>
28	Riya Vikas Urdeo	SE	<i>Urdeo</i>
29	Seva Srinjay Chalke	TE	<i>Chalke</i>
30	Prerana Gouthika Sawant	SE	<i>Sawant</i>

Dr. Khedekar Sachin R  
HOD, ETC

Attendance sheet <i>Webinar on VLSI/Semiconductor Industry</i>			
		DATE: 05/08/2024	
SR.NO.	NAME OF STUDENTS	CLASS SE/TE/BE	Sign
1	Sanika chauhan	SE	<i>Chauhan</i>
2	achal shetare	SE	<i>Shetare</i>
3	Adutva s. kadam	SE	<i>Kadam</i>
4	Bhagyashri s. kherade	SE	<i>Kherade</i>
5	Sayali s. mohite	TE	<i>Mohite</i>
6	Vaishnavi V. Gorvale	TE	<i>Gorvale</i>
7	Sanika A. Bolade	TE	<i>Bolade</i>
8	Amey. D. shembekar	SE	<i>Shembekar</i>
9	vishva . U. Jangam.	SE	<i>Jangam</i>
10	Saethak. S. Sawant	SE	<i>Sawant</i>
11	kunal . s. patil	SE	<i>Patil</i>
12	Amit. B. Kokate.	SE	<i>Kokate</i>
13	Arsh. A. Chiplunkar	SE	<i>Chiplunkar</i>
14	Santak mohite	TE	<i>Mohite</i>
15	Aditya .R. Jhinde	BE	<i>Jhinde</i>
16	Prashant. R. Jangam	BE	<i>Jangam</i>
17	Manish .M. Jhinde	TE	<i>Jhinde</i>
18	Varushabh. B. Shirke	TE	<i>Shirke</i>
19	Dinard. N. Taldenkar	TE	<i>Taldenkar</i>
20	Rutik. D. mali	TE	<i>Mali</i>
21	Saurabh S. Navrat	TE	<i>Navrat</i>
22	dhonanjay .C. Gatal	SE	<i>Gatal</i>
23	Maksand M. Sakpal.	SE	<i>Sakpal</i>
24	Sohil .Y. Kadam	SE	<i>Kadam</i>
25	Pratimraju. wani	SE	<i>Wani</i>
26	Bhavik. Mang. Pawarukar	SE	<i>Pawarukar</i>
27			
28			
29			
30			

Dr. Khedekar Sachin R  
HOD, ETC

This Webinar was covered Semiconductor Industry introduction, details about available placement opportunities, briefing about each topic, doubt clarification. The webinar was very knowledgeable, interactive and informative. Total 56 Students and 02 Faculties attended the Seminar with good enthusiasm.

### **Report of Seminar on How to write research/technical paper**

The department of E&TC has successfully conducted a Seminar on How to write research/technical paper. The following are the details of the same.

Date: 07/08/2024

Time: 11.00 Am to 12.30 Pm

Venue: BE Classroom

Total Number of students attended the seminar: 45

Name of the staff Co-ordinators: Mr. S. N. Pandharkame

Name of the resource person: Dr. S. H. Gharat, Associate Professor, GIT.

Writing a research paper can be a daunting task, especially for those new to academic writing. The process involves not just gathering and analyzing information but also organizing and presenting it in a clear, logical, and compelling way. A well-written research paper not only contributes to the academic community but also demonstrates the author's critical thinking and research skills. This guide aims to demystify the research paper writing process by breaking it down into manageable steps, from selecting a topic and conducting a literature review to crafting a strong thesis statement, organizing the paper's structure, and ensuring proper citation of sources. By following these steps, writers can produce a polished and coherent research paper that effectively communicates their findings and arguments.

Some snaps of the same are shown below.





Attendance of the seminar is shown below.

Attendance sheet Seminar on How to write technical/ Research paper			
SR.NO.	NAME OF STUDENTS	CLASS SE/TE/BE	Sign
1	Mustkan M. Bandarkar	BE	
2	Ayan G. Dholke	BE	
3	Yashir Bagdade	BE	
4	Sohel Sayyad	BE	
5	Prash P. Phalke	BE	
6	Ayfon I. Mahaldar	BE	
7	Zaid Naeem Ansari	BE	
8	Nihal T. Boat	SE	
9	Suhil Vashwani Kudum	SE	
10	Omkar Maruti Desai	TE	
11	Omkar Chandrahas Deshmukh	TE	
12	Manish Mengaj Shinde	TE	
13	Saumabh Sushil Navrat	TE	
14	Vrushabh Bharat Shinde	TE	
15	Yash Gajap	TE	
16	Aditya R. Shinde	BE	
17	Venkatesh Gavai	BE	
18	Poashant B. Jangam	BE	
19	Fidan U. Jambharkar	BE	
20	Madhura M. Jathar	TE	
21	Kirti B. Naik	TE	
22	Prerana G. Sawalkar	SE	
23	Madhura B. Hattikate	SE	
24	Anushka C. Mahadik	SE	
25	Bhagyashri S. Kherade	SE	
26	Aparna S. Kadam	SE	
27	Bhavik Manoj Powarsukar	SE	
28	Sasthak Sanjay Savant	SE	
29	Kunal Suresh Patil	SE	
30	Ayush Arvind Khambe	SE	

Dr. Khedekar Sachin R  
HOD, ETC

Attendance sheet Seminar on How to write technical/ Research paper			
SR.NO.	NAME OF STUDENTS	CLASS SE/TE/BE	Sign
1	Taldevkar Nitin	TE	
2	Sakpal Makrand Mangesh	SE	
3	Sayali Santosh Mohit	TE	
4	Sumika Anant Balade	TE	
5	Veekhvi Vijay Gorivule	TE	
6	Saloni Pradip Nagarkar	TE	
7	Seva Sanjay Chalke	TE	
8	Riya Vikas Udeg	SE	
9	Ayushigshak Shinde	TE	
10	Akhal Shukla	SE	
11	Triveni Mahadev	TE	
12	Purna Rasal	TE	
13	Anushka Kadam	TE	
14	Anita Dhedic	TE	
15	Sudashini Shinde	SE	
16			
17			
18			
19			
20			
21			
22			
23			
24			
25			
26			
27			
28			
29			
30			

Dr. Khedekar Sachin R  
HOD, ETC

Writing a research paper is a structured process that, when approached methodically, can lead to the creation of a clear, persuasive, and well-organized academic work. By carefully selecting a topic, conducting thorough research, developing a strong thesis statement, and organizing your findings into a coherent structure, you can effectively communicate your ideas and contribute meaningfully to the academic community. Proper citation of sources and a clear writing style further ensure that your work is credible and accessible. Ultimately, mastering the art of research paper writing not only enhances your academic skills but also empowers you to engage with complex ideas and share your insights with others.

### **Report of Webinar on Advances in RedHat Linux and DevOps**

The department of E&TC has successfully conducted a online webinar on Advances in RedHat Linux and DevOps. The following are the details of the same.

Date: 09/08/2024

Time: 11.00 Am to 12.30 Pm

Venue: BE Classroom

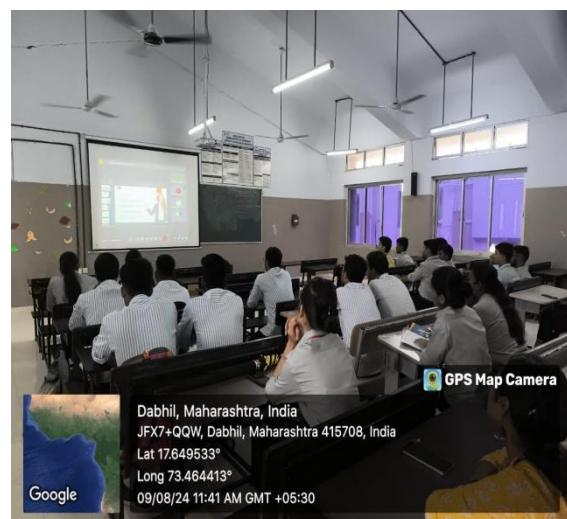
Total Number of students attended the seminar: 30

Name of the staff Co-ordinators: Mr. T. L. Iltapawar & Mr. S. N. Pandharkame

Name of the resource person: Mrs. Bhagyashree Bhat, Director, BITS, Pune.

The Red Hat Certification program is a globally recognized credentialing system for professionals seeking to validate their expertise in Linux and open-source technologies. This webinar provides an in-depth analysis of the Red Hat Certification program, highlighting its significance, available certifications, benefits, and the impact it has on career advancement within the IT industries.

Some snaps of the same are shown below.



Attendance of the seminar is shown below.

Attendance sheet		Workshop on 'Advances in Redhat Linux & DevOps'	
SR.NO.	NAME OF STUDENTS	DATE:	Sign
1	Ayan G. Dhokle	9 AUG 2024	Admited
2	Yashas Bagdade		Yashas
3	Sonika Amte		Sonika
4	Taivani Mahadikar		Taivani
5	Yash S. Ghag		Yash
6	Swapnil A. Ghankar		Swapnil
7	Sohel M. Sayyad		Sohel
8	Piyush P. Phalke		Piyush
9	Janhavi S. Chile		Janhavi
10	Ayushri Shinde		Ayushri
11	Madhura Jathar		Madhura
12	Kirti Naik		Kirti
13	Anushta Kadam		Anushta
14	Amita Bhadre		Amita
15	Ayton Mahaldar		Ayton
16	Fidyan Jamthastkar		Fidyan
17	Zaid Ansari		Zaid
18	Yash Gtag		Yash
19	Vrushali Shinde		Vrushali
20	Nirad Taldar Kar		Nirad
21	Nainesh Benere		Nainesh
22	Rutik Santosh Gamare		Rutik
23	Manish M. Shinde		Manish
24	Omkar C. Deshmukh		Omkar
25	Omkar M. Desai		Omkar
26	Vaibhavi V. Gorivale		Vaibhavi
27	Sayali S. Manite		Sayali
28	Seva S. Chalke		Seva
29	Saloni Nagarkar		Saloni
30	Poashruti R. Jangam		Poashruti

Good luck  
Time coordinator

Dr. Khedekar Sachin R  
HOD, EXTC

The RedHat certification program plays a pivotal role in shaping the careers of IT professional by validating their Linux and open-source skills. With its globally recognized certifications, practical exam format, and industry relevance, RedHat Certifications serves as a valuable asset for career advancement and professional development in the ever-evolving world of technology

### **Report of seminar on 'NOC, SOC opportunities and various Certification paths '**

The department of E&TC has successfully conducted a seminar on NOC, SOC opportunities and various Certification paths. The following are the details of the same.

Date: 16/08/2024

Time: 10.00 Am to 12.00 Pm

Venue: BE Classroom

Total Number of students attended the seminar: 19

Name of the staff Co-ordinators: Dr. S. R. Khedekar & Mr. S. N. Pandharkame

Name of the resource person: Mr. Suyog Bedekar, Lead Consultant Infosys Technologies Ltd, Pune.

Network on Chip (NoC) and System on Chip (SoC) are pivotal technologies in modern electronics, driving advancements in areas like consumer electronics, automotive systems, IoT, and telecommunications. SoCs integrate all components of a computer system onto a single chip, enabling compact and efficient devices such as Smartphone, wearable, and medical devices. NoC, on the other hand, addresses communication challenges within SoCs by providing a scalable and efficient network, essential for high-performance computing, multi-core processors, and AI systems. Career opportunities in NoC and SoC are vast, ranging from roles in VLSI design, embedded systems, and semiconductor manufacturing to positions in advanced research and development. To excel in these fields, professionals can pursue certifications such as the Xilinx Certified FPGA Design Engineer, ARM Accredited Engineer, or Cadence and Synopsys tool certifications.

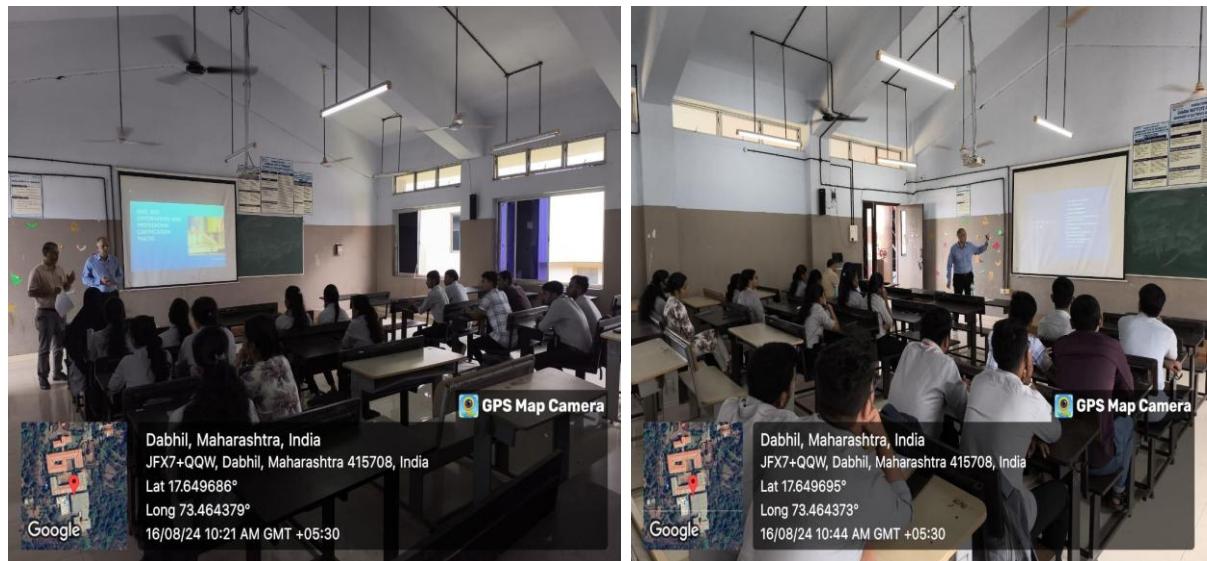
Attendance of the seminar is shown below.

Attendance sheet			
Seminar on NUC, SOC opportunities & various certification paths			DATE: 16/08/24 10-12 pm
SR.NO.	NAME OF STUDENTS	CLASS SE/TE/BE	Sign
1	Muskan M. Bandarkar	BE	Muskan
2	Sanika Dhananjay Nayakal	TE	Sanika
3	Triveni Jagdish mahadik	TE	Triveni
4	Madhura M. Jathar	TE	Madhura
5	Kirti Baburao Naik	T.E	Kirti
6	Janhavi Sandip Chle	TE	Janhavi
7	Paorna Suhas Rasal	T.E.	Paorna
8	Riya Vikas Odle	S.E	Riya
9	Bhagyashri Santosh Khedekar	S.F	Bhagyashri
10	Apurva Shrikrishna Kadam	S.F	Apurva
11	Makrand Mangesh Sakpal	S.E	Makpal
12	Harshad Vijay chogale	S.E	Harshad
13	Kedar Sandip Awale	D.S.E.	Kedar
14	Manish Manoj Shinde	T.E	Manish
15	Anilkar Chandrabhar Deshmukh	T.F	Anilkar
16	Yash valbhav Patil	T.E	Yash
17	Vishva Uttam Tangam	S.E.	Vishva
18	Sudarshan Shankar Shinde	S.E.	Sudarshan
19	Nihal Tqbal Bora	S.E	Nihal
20			
21			
22			
23			
24			
25			
26			
27			
28			
29			
30			

Dr. Khedekar Sachin R  
HOD, ETC

Some snaps of the same are shown below.





This seminar covers the NOC & SOC fundamentals, design principles, NOC & SOC processes, CISCO Certification, Juniper Certification, Microsoft Certification, AWS Certification, challenges, and applications of these technologies in modern integrated circuits.

## **REPORT ON THE TRAFFIC AWARENESS PROGRAM**

Organized by: Disaster Management Cell, Gharda Institute of Technology, Lavel

In Association with: Traffic Police, Maharashtra Government Helpline Centre, Chiplun

Date: 23rd January 2025

Time: 11:15 AM to 12:30 PM

### **1. Introduction**

A traffic awareness program was held at Gharda Institute of Technology, Lavel, by the Disaster Management Cell and the Maharashtra Government Helpline Centre, Chiplun. In this session, students were taught traffic safety rules, how to help others in an accident, and how to help others during an accident. The event was led by Prof. Nitish Dattatray Galande, Chairman of the Disaster Management Cell. The main speakers were PSI Mr. Anant Patil and Traffic Constable Mr. Surendra Kadam. In all, around 150 students attended the event. Students can use the knowledge they gain from the program by adhering to traffic rules and practicing safe driving habits.

Additionally, they can educate their peers and local communities about the importance of following traffic regulations, which will help them become advocates for road safety. Additionally, students can be prepared to assist in emergencies by knowing basic first aid techniques and how to contact the relevant authorities.

### **2. Objectives**

1. In order to make students aware of traffic rules and signals.
2. In order to explain which documents are required while driving.
3. To share safe driving tips and prevent accidents.
4. The purpose of this course is to teach students how to assist injured people.

### 3. Event Details

Arrangements: The event was technically supported by Prof. Pandharkame, Mr. Pravin Patil and Mr. Ninad Taldevkar (TE-EXTC). The program began with an introduction by Prof. Nitish Dattatray Galande, who emphasized the importance of traffic awareness. This was followed by a session led by PSI ,Mr. Anant Patil, who discussed essential traffic rules and the necessary documents for driving. Traffic Constable Mr. Surendra Kadam concluded the event with a demonstration on safe driving practices and a brief workshop on basic first aid techniques for accident scenarios. Traffic safety education is crucial in shaping responsible and informed drivers, especially among young individuals who are new to the road. By instilling a strong understanding of traffic rules and safe driving practices, such programs can significantly decrease the likelihood of accidents and promote a culture of safety. Furthermore, equipping students with first aid skills enables them to act effectively in emergency situations, potentially saving lives and fostering a sense of community responsibility.

### 4. Key Points Discussed:

- o Importance of following traffic signals and rules.
- Documents required for driving (license, insurance, etc.).
- o Safety tips for driving and avoiding accidents.
- o How to assist accident victims and ensure they get help.

The session was very useful and informative.

### 5. Outcomes

1. Students learned the importance of traffic rules and safety.
2. They understood which documents are needed while driving.
3. The session encouraged students to help accident victims.
4. It created awareness about accident prevention and driving safely.

### 6. Acknowledgments

The Disaster Management Cell, faculty, and student coordinators worked hard to succeed.



Conclusion Traffic Awareness helps students learn about the importance of safe driving and road safety. As well as motivating them to follow traffic rules, it also helps them to provide support to others in need. These events contribute greatly to making roads safer. The Traffic Awareness Program focused on the importance of adhering to traffic rules, understanding the necessary driving documents, and practicing safe driving techniques. Furthermore, it emphasized the importance of first aid skills in emergencies and encouraged students to offer assistance to accident victims. As a result of the program, young drivers will develop a culture of safety and responsibility, ultimately contributing to a safer road environment.

## **Report of Seminar on MASTERING MANNERS and ETIQUETTE ABOUT WOMEN.**

The department of E&TC has successfully conducted a free seminar on MASTERING MANNERS and ETIQUETTE ABOUT WOMEN. The following are the details of the same.

Date: 04/02/2025

Time: -11:00 PM

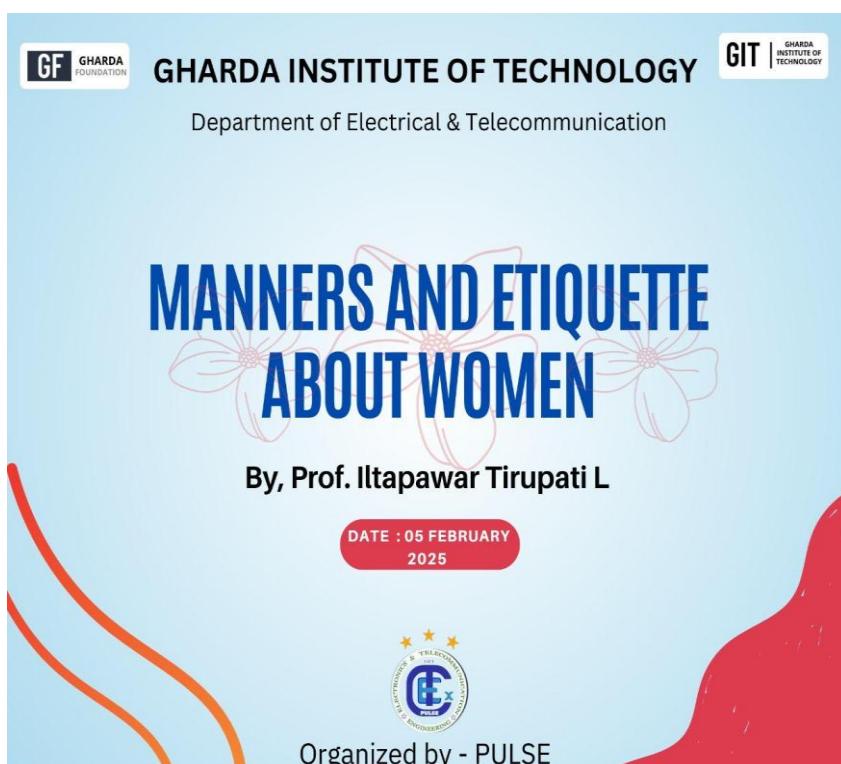
Venue:-BE classroom (EXTC)

Total No of students attended the seminar -27 ( SE TE and BE EXTC )

Name of the staff Co-coordinators:- Prof. S. N. Pandharkame

Name of the Resource Person:-Prof. Iltapawar Tirupati L

This report provides a comprehensive overview of some information about MASTERING MANNERS and ETIQUETTE ABOUT WOMEN. Good manners and etiquette are essential for men in their interactions with women, whether in personal, social, or professional settings. Respect, kindness, and understanding form the foundation of positive relation.

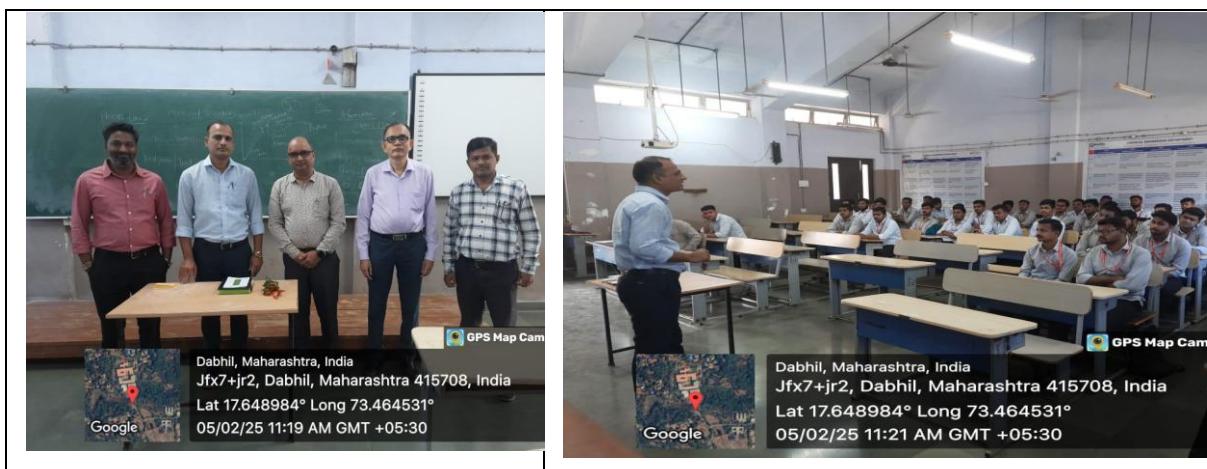


Attendance of seminar

EXTC (WTC)			
Attendance sheet			
DATE:			
SR.NO.	NAME OF STUDENTS	CLASS SE/TE/BE	Sign
1	Sarthak S. Sawant	SE	<i>Sawant</i>
2	Kunal S. Patil	SE	<i>Patil</i>
3	Kedar Sandip Awale	S.E	<i>Kedar</i>
4	Jawwad A.S. Kazi	S.E	<i>Jawwad</i>
5	Ninad Nitin Talderkar	T.E	<i>Ninad</i>
6	Sarthak Vitthal Mohite	T.E	<i>Mohite</i>
7	Makrand Mangesh Sakpal	S.E	<i>Sakpal</i>
8	Sudarshan Shankar Shinde	S.E	<i>Shinde</i>
9	Yash Vithal Patil	T.E	<i>Yash</i>
10	Armen Dattatray Shembekar	S.E	<i>Shembekar</i>
11	Vishnu Utam Jangam	S.E	<i>Jangam</i>
12	Sarthak Sanjay Sawant	SE	<i>Sawant</i>
13	Kunal Suresh Patil	SE	<i>Patil</i>
14	Bhavik Manoj Pawaskar	S.E	<i>Pawaskar</i>
15	Dharan Jay Chandrakant Gotal	SE	<i>Gotal</i>
16	Sahil Yashwant Tadam	SE	<i>Tadam</i>
17	Ayush Arvind Thambr	SE	<i>(A) A.K.</i>
18	Nihal Tigmals Raut	S.E (EXTC)	<i>Raut</i>
19	Keshav Prabhu	BE (EXTC)	<i>Prabhu</i>
20	Sahil Lambe	BE (EXTC)	<i>Lambe</i>
21	Chauhan Ritesh Kumar	B.E (EXTC)	<i>Ritesh</i>
22	Piyush Phadke	B.E (EXTC)	<i>P. P. Phadke</i>
23	Omkar Deshmukh	T.E (EXTC)	<i>Deshmukh</i>
24	Vrushabh Shirke	TE (EXTC)	<i>Shirke</i>
25	Omkar Desai	TE (EXTC)	<i>Desai</i>
26	Yash Shug	BE (I)	<i>Yash</i>
27	Swapnil Chankar	BE	<i>Chankar</i>
28			
29			
30			

*Dr. Khedekar Sachin R  
HOD, EXTC*

Some snaps of the same are shown below.



Mastering manners and etiquette when interacting with women is not just about following rules; it is about fostering a culture of mutual respect, understanding, and equality. Men who

practice good etiquette demonstrate emotional intelligence, empathy, and social awareness, which positively impact both personal and professional relationships.

By being courteous, listening actively, and treating women as equals, men contribute to a more inclusive and respectful society. Small gestures, such as acknowledging a woman's opinions, respecting her space, and valuing her contributions, go a long way in building trust and harmony.

Ultimately, etiquette is not about chivalry or outdated traditions but about creating an environment where all individuals—regardless of gender—feel valued, safe, and respected. The goal is to move beyond stereotypes and embrace a mindset that supports fairness, kindness, and dignity in every interaction.

### **Report of seminar on 'Gender Sensitization'**

The department of E&TC has successfully conducted a seminar on Gender Sensitization. The following are the details of the same.

Date: 22/03/2024

Time: -11:00 AM

Venue:-BE classroom (EXTC)

Total No of students attended the webinar -28 (B.E,T.E and S.E EXTC )

Name of the staff Co-coordinators:- Prof. S. N. Pandharkame,

Name of the Resource Person:- Prof.V.S.Kolge

The Seminar on Gender Sensitization was organized with the aim raising awareness of gender equality concerns and changing people's attitude and behaviors towards other genders and oneself. To educate and promote awareness between college girls students, a seminar on Gender Sensitization was conducted by Pulse Committee. This report summarizes the key insights and takeaways from the event. The banner Designed for the same and attendance of the seminar is shown below.

**GHARDA INSTITUTE OF TECHNOLOGY**

Department of Electronics and Telecommunication

Seminar on

**GENDER SENSITIZATION**

By Prof. V.S.Kolge

Date: 22 March 2024



ORGANIZED BY PULSE

Attendance sheet (Gender Sensitization)			
SR. NO.	NAME OF STUDENTS	CLASS SE/TE/BE	Sign
1	Seva Sanjay Chalke	SE	Seva
2	Amita Monohar Dhadve	SE	Dhadve
3	Sayali Santosh Mohite	SE	Sayali
4	Sonika Dhananjay Nayakal	SE	Sonika
5	Gauri Jitendra Yerunkar	TE	Gauri
6	Aayushi Khinde	SE	Aayushi
7	Ashal Mule	SE	Ashal
8	Girija Santosh Kangutkar	BE	Girija
9	Gauri Santosh Kangutkar	BE	Gauri
10	Sonika Arvind Burembandkar	BE	Sonika
11	Kiran Prabhakar Ghatol	BE	Kiran
12	Mayuri Nitin Patil	BE	Mayuri
13	Siddhi Sachin Patil	BE	Siddhi
14	Anjali Kumar More	BE	Anjali
15	Prajakta Nagesh Chavan	BE	Prajakta
16	Madhura Mayur Jathar	SE	Madhura
17	Kirti Baburao Naik	SE	Kirti
18	Saumya S. Chitre	TE	Saumya
19	Mukund M. Bandarkar	TE	Mukund
20	Purna Suhas Rasal	SE	Purna
21	Reya Ilibus Udes	SE	Reya
22	Chirmayee Anur Ingavale	BE	Chirmayee
23	Mansi Mengesh Telange	BE	Mansi
24	Rudali Rajendra Joshi	BE	Rudali
25	Vanshita Sunil Kadam	BE	Vanshita

Prof. Pandharkarne Subodh N  
HOD, EXTC  
Pulse Co-Ordinator

Dr. Khedekar Sachin R  
HOD, EXTC  
Event Coordinator

Attendance sheet			
SR. NO.	NAME OF STUDENTS	CLASS SE/TE/BE	Sign
1	Mrunmayi S. Bhosale	BE	Mrunmayi
2	Vaibhavi V. Gorivalo	SE	Vaibhavi
3	Saloni Nagarjar	SE	Saloni
4			
5			
6			
7			
8			
9			
10			
11			
12			
13			
14			
15			
16			
17			
18			
19			
20			
21			
22			
23			
24			
25			

Prof. Pandharkarne Subodh N  
HOD, EXTC  
Pulse Co-Ordinator

Dr. Khedekar Sachin R  
HOD, EXTC  
Event Coordinator

Some snaps of the same are shown below.





The Seminar on Gender Sensitization successfully achieved its objectives by providing participants by examining people's personal attitudes, beliefs and questioning the 'realities' they thought they know. The event contributed to building a foundation on person, regardless of their gender identity deserves respect and equal opportunities.

### **Report of Seminar on "Data Science & Analytics"**

The department of E&TC has successfully conducted a free seminar on "Data Science & Analytics". The following are the details of the same.

Date: 12/02/2025

Time: -11:00 AM - 1:00 PM

Venue:-BE classroom (EXTC)

Total No of students attended the webinar - ( BE and TE EXTC )

Name of the staff Co-coordinators:- Prof. S. N. Pandharkame

This report provides a comprehensive overview of Data Science & Analytics by Dr. Anand Kolharkar, Dr. Anand Kolharkar is a distinguished industry expert and the founder of Opine Group. With an extensive career spanning over 28 years, shared his deep expertise with the attendees. He emphasized the transformative power of data in driving business growth, highlighting several key areas:

**Customer Behavior Analysis:** Understanding customer patterns to tailor services and products effectively.

**Structured Methodologies:** The importance of implementing systematic approaches in data analysis to ensure accuracy and reliability.



Some snaps of the same are shown below.





In Conclusion , Data Science and Analytics are revolutionizing industries by enabling smarter, data-driven decisions. Experts like Dr. Anand Kolharkar contribute significantly by developing BI practices, training professionals, and driving innovation. As businesses continue to embrace data-driven strategies, these fields will play an increasingly vital role in shaping the future. Overall, the session successfully bridged the gap between theoretical knowledge and practical applications, reinforcing the significance of data science in shaping the future of various sectors.

### **Report on ‘Industrial Visit’**

The department of E&TC has successfully conducted an Industrial visit.

The following are the details of the same.

Date: 14/02/2025

Time:11.00 Am

### **Venue:-Seiler Garepa India Pvt. Ltd. At- Fansu, Dapoli**

The Industrial visit at Seiler Garepa India Pvt. Ltd. was highly engaging and intellectually stimulating event that took place on 14 Feb 2025 organized by EXTC Department. This Industrial visit aim to gain the practical insights into the operations and process of esteemed company and to better understand the manufacturing of helium leak test machines, helium recovery equipment and assembly lines.

### **Helium Leak Test Machines:**

Helium leak test machines are used to detect and quantify leaks in various sealed components and systems. The process typically involves the following steps:

## **1. Preparation and Setup**

- The test object (e.g., a sealed component, pipeline, or vacuum chamber) is placed in a test chamber or connected to the test setup.
- The system is evacuated to create a vacuum if required.
- Helium gas is used as a tracer gas because of its small atomic size and inert nature.

## **2. Testing Methods**

Helium leak testing can be performed using different methods:

### **A. Vacuum (Sniffer) Method**

- The test object is evacuated to create a vacuum inside.
- Helium gas is sprayed externally around potential leak points.
- A mass spectrometer-based helium detector inside the test object detects any helium that enters through leaks.

### **B. Pressure (Accumulation) Method**

- The test object is pressurized with a helium-air mixture.
- A helium detector probes the external surface for escaping gas.
- This method is used when vacuum conditions are not feasible.

### **C. Helium Bombing Method (for Hermetically Sealed Parts)**

- The sealed part is placed in a high-pressure helium atmosphere for a set duration.
- If there are micro-leaks, helium penetrates inside.
- The part is then placed in a vacuum chamber, and a helium detector measures the gas escaping from inside.

### **D. Integral (Vacuum Chamber) Method**

- The test object is placed inside a vacuum chamber.

- The object is pressurized with helium internally.
- Any leaks cause helium to escape into the vacuum chamber, where a helium mass spectrometer detects it.

### **3. Leak Detection & Measurement**

- The helium leak detector (typically a mass spectrometer) identifies the presence and concentration of helium.
- The leak rate is measured, usually in units of atm-cc/sec, mbar-L/sec, or ppm.
- Acceptable leakage thresholds depend on industry standards and application requirements.

### **4. Results & Interpretation**

- The system determines if the component passes or fails based on predefined leak rate limits.
- Results are logged for quality assurance and traceability.

### **5. Post-Test Procedures**

- If a leak is found, corrective actions such as sealing, re-welding, or component replacement may be taken.
- If the part passes, it proceeds to the next stage in production or deployment.

#### **Helium Recovery Equipment:**

Helium recovery equipment is used to collect, purify, and reuse helium gas, which helps reduce costs and prevent waste. When helium is used in processes like leak testing, cryogenics, or manufacturing, it is often lost into the air. A recovery system captures this helium, removes impurities, compresses it, and stores it for future use.

The system typically includes a collection unit that gathers the used helium, a compressor to pressurize it, a purification unit to filter out unwanted gases, and a storage tank to hold the clean helium. Once purified, the helium can be used again in the same process.

There are different types of helium recovery systems. Some only collect the gas but don't recycle it, while others fully clean and reuse it in a closed loop. Some systems mix recovered helium with fresh helium to maintain quality.

These systems are widely used in industries like aerospace, medical imaging (MRI machines), semiconductor manufacturing, and automotive testing. Using helium recovery not only saves money but also helps preserve this valuable gas for future needs.

### **Role of IoT in Helium Leak Test Machines**

IoT (Internet of Things) enhances the functionality and efficiency of helium leak test machines by enabling real-time data collection, monitoring, and predictive maintenance.

#### ***Integration Points:***

- **Remote Monitoring:** Sensors connected to the test machine relay real-time data about pressure, helium concentration, and leakage to a centralized cloud server.
- **Predictive Maintenance:** IoT systems analyze machine data and predict potential failures, minimizing downtime.
- **Data Analytics & Insights:** IoT platforms provide data visualization to help improve testing efficiency and process control.
- **Automated Alerts/Notifications:** Alerts are sent to operators if leakage exceeds permissible limits, ensuring prompt corrective actions.

#### ***Benefits:***

- Increased efficiency and reliability.
- Reduced human intervention and error.
- Faster identification of leaks and real-time response.
- Enhanced traceability and compliance reporting.

### **Role of Embedded Systems in Helium Leak Test Machines**

Embedded systems control and automate various functionalities of helium leak testing machines. These systems involve microcontrollers or microprocessors programmed to handle the core tasks of the test machine.

### ***Key Functions:***

- **Signal Acquisition & Processing:** Embedded systems read data from pressure sensors, helium detectors, and other test sensors.
- **Control Logic Implementation:** They execute test algorithms and control vacuum pumps, valves, and gas flow.
- **User Interface (UI):** Embedded systems provide intuitive interfaces via touch screens or display panels for operators to configure and monitor test parameters.
- **Communication Protocols:** Embedded systems enable communication between the leak test machine and IoT platforms via protocols such as MQTT, HTTP, or Mod bus.

### ***Microcontroller/Processor Role:***

- ADC/DAC for sensor data acquisition.
- Real-time operating systems (RTOS) for time-critical operations.
- Communication modules (Wi-Fi, Ethernet, Bluetooth) for IoT connectivity.

## **IoT and Embedded System Synergy in Helium Leak Testing**

IoT and embedded systems work together to improve the performance of helium leak testing machines.

### ***Data Flow:***

- **Sensor Data Acquisition:** Embedded systems collect sensor data.
- **Local Processing & Control:** Embedded firmware controls the machine and performs initial analysis.
- **IoT Cloud Transmission:** Relevant data is transmitted to an IoT platform for advanced analysis and remote monitoring.
- **Action & Feedback Loop:** IoT platforms send control commands or alerts based on analyzed data, ensuring automated corrective measures.

Some photos of same are shown below,





The industrial visit to Seiler Garepa India Pvt. Ltd. was an enlightening experience, providing valuable insights into the manufacturing industry. We all were impressed by the company's commitment to quality, safety, and environmental responsibility.

### **Placement in 2024-25**

SR. NO	NAME	Company name	Package
1	Ayfan Mahaldar	Q-Spider	2.5 L
2	Keshav Prabhu	Q-Spider	2.5 L
3	Aditya Shinde	Q-Spider	2.5 L
4	Sakshi Chile	Quality Kiosk	2.5 L
5	Swapnil Ghanekar	Excelr	2.5 L
6	Prashant Jangam	Laxmi Oraganic Industries Ltd	2.5 L